

GL Silicon N-Channel Power MOSFET

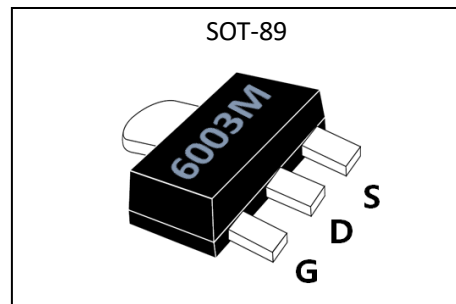
General Description

The GL6003M uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications. The package form is SOT-89, which accords with the RoHS standard.

V_{DSS}	60	V
I_D	3.0	A
P_D	1.7	W
$R_{DS(ON)}$	105	mΩ

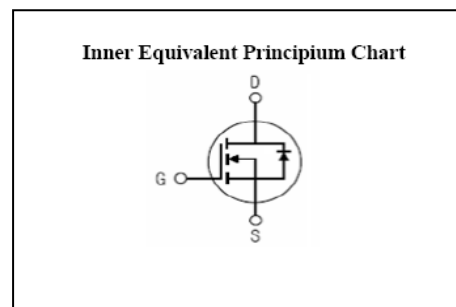
Features

- Fast Switching
- Low Gate Charge and Rdson
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test



Applications

- PWM applications
- Load switch
- Power management



Absolute (Tc=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	60	V
I_D	Continuous Drain Current	3.0	A
	Continuous Drain Current ($T_C=70\text{ }^{\circ}\text{C}$)	2.4	A
I_{DM}^{a1}	Pulsed Drain Current	10	A
V_{GS}	Gate-to-Source Voltage	± 20	V
dv/dt^{a3}	Peak Diode Recovery dv/dt	5.0	V/ns
P_D	Power Dissipation	1.7	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	$^{\circ}\text{C}$
T_L	Maximum Temperature for Soldering	300	$^{\circ}\text{C}$

Caution Stresses greater than those in the "Absolute Maximum Ratings" may cause permanent damage to the device

Thermal Characteristics

Symbol	Parameter	Typ.	Units
$R_{\theta JA}$	Junction-to-Ambient	74	$^{\circ}\text{C}/\text{W}$

**GL Silicon N-Channel Power MOSFET****Electrical Characteristics** ($T_c=25^{\circ}\text{C}$ unless otherwise specified)

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	60	--	--	V
$\Delta BV_{DSS}/\Delta T_J$	Bvdss Temperature Coefficient	$I_D=-250\mu A, \text{Reference } 25^{\circ}\text{C}$	--	0.1	--	V/ $^{\circ}\text{C}$
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=60V, V_{GS}=0V, T_a=25^{\circ}\text{C}$	--	--	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_a=125^{\circ}\text{C}$	--	--	250	
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+20V$	--	--	1	μA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-20V$	--	--	-1	μA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10V, I_D=3.0A$	--	78	100	m Ω
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=4.5V, I_D=3.0A$	--	95	130	m Ω
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.3	2.0	V
Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g_{fs}	Forward Transconductance	$V_{DS}=15V, I_D=2.0A$	3	--	--	S
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=30V$ $f=1.0\text{MHz}$	--	250	--	pF
C_{oss}	Output Capacitance		--	35	--	
C_{rss}	Reverse Transfer Capacitance		--	20	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D=1.5A, V_{DD}=30V$ $V_{GS}=10V, R_G=1.0\Omega$	--	6.0	--	ns
t_r	Rise Time		--	15	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	15	--	
t_f	Fall Time		--	10	--	
Q_g	Total Gate Charge	$I_D=3.0A, V_{DD}=30V$ $V_{GS}=10V$	--	6.0	--	nC
Q_{gs}	Gate to Source Charge		--	1.0	--	
Q_{gd}	Gate to Drain ("Miller") Charge		--	1.3	--	

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)		--	--	3.0	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	10	A
V_{SD}	Diode Forward Voltage	$I_S=3.0A, V_{GS}=0V$	--	--	1.5	V
t_{rr}	Reverse Recovery Time	$I_S=3.0A, T_J=25^\circ C$	--	70	--	ns
Q_{rr}	Reverse Recovery Charge	$di_F/dt=100A/us, V_{GS}=0V$	--	140	--	nC

Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$

^{a1}: Repetitive rating; pulse width limited by maximum junction temperature

^{a3}: $I_{SD}=3.0A, di/dt \leq 100A/us, V_{DD} \leq BV_{DS}, \text{Start } T_J=25^\circ C$

Test Circuits

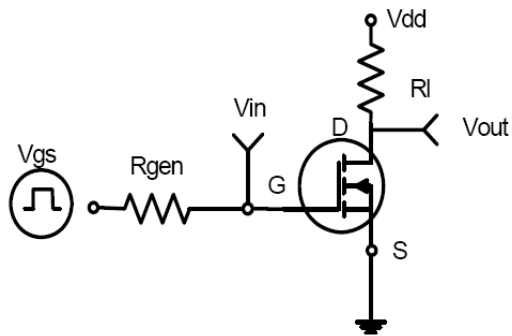


Figure 1: Switching Test Circuit

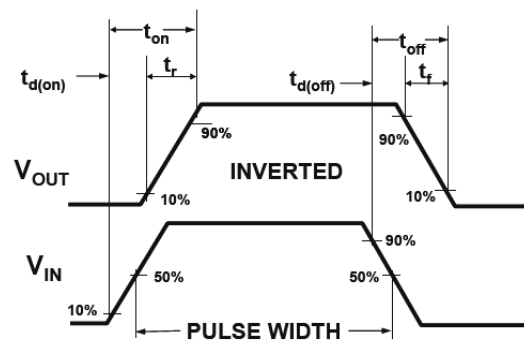


Figure 2: Switching Waveforms

Characteristics Curves

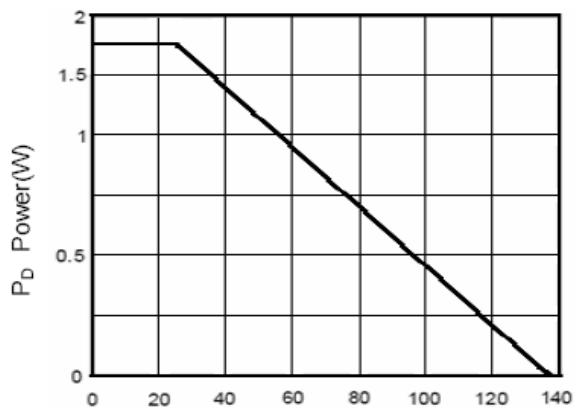


Figure 3 Power Dissipation

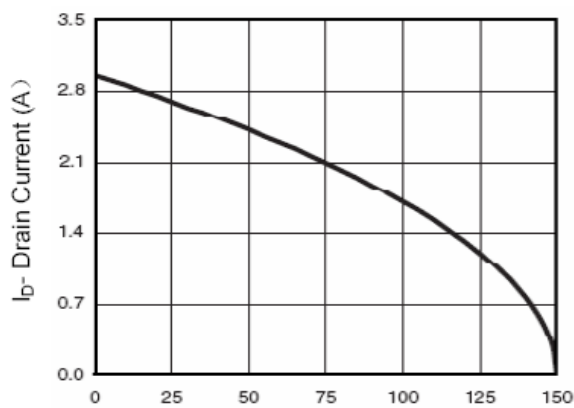


Figure 4 Drain Current

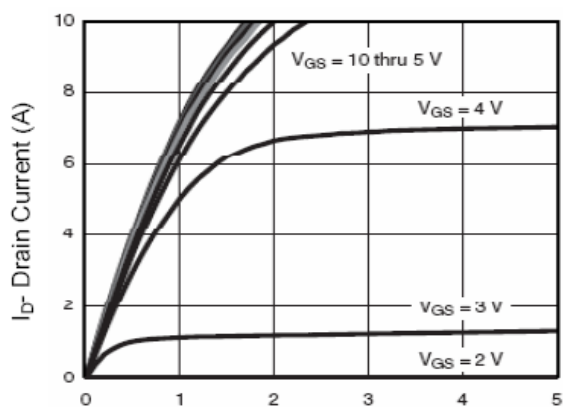


Figure 5 Output Characteristics

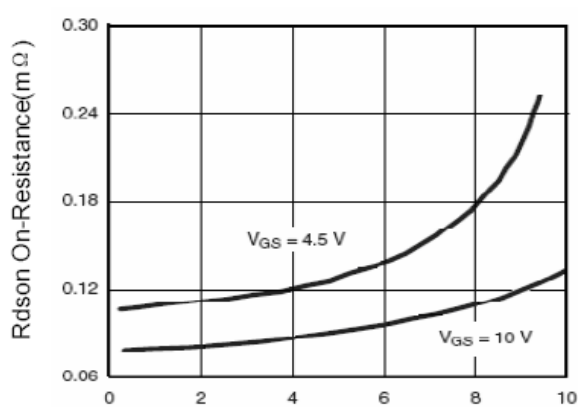


Figure 6 Drain-Source On-Resistance

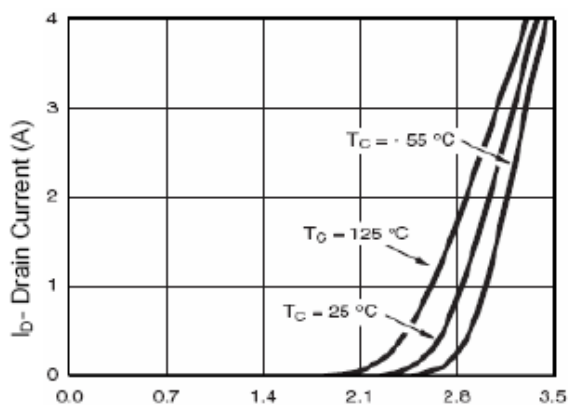


Figure 7 Transfer Characteristics

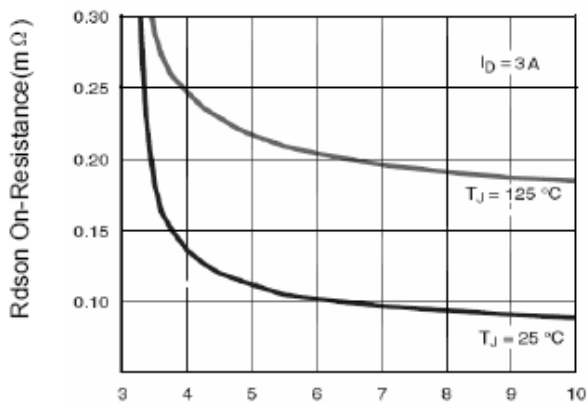


Figure 9 $R_{DS(on)}$ vs V_{GS}

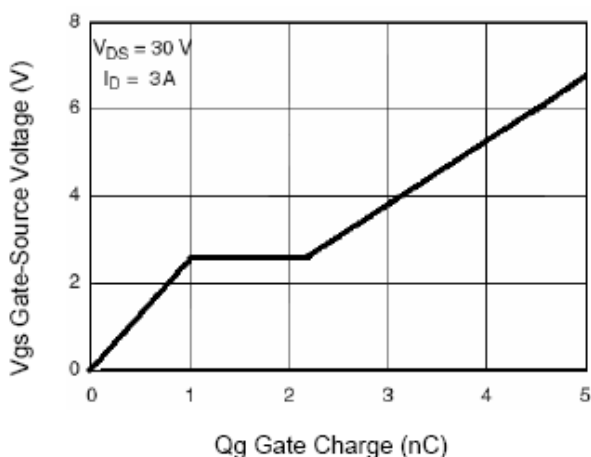


Figure 11 Gate Charge

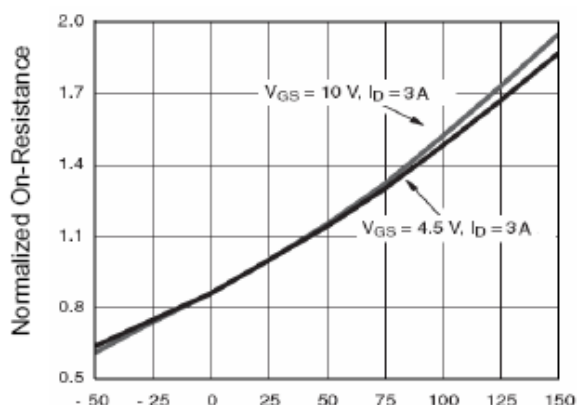


Figure 8 Drain-Source On-Resistance

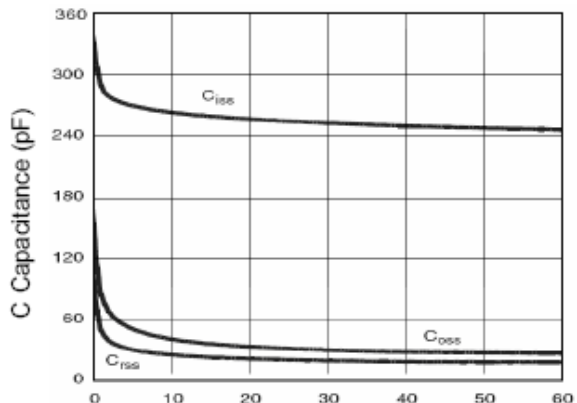


Figure 10 Capacitance vs V_{DS}

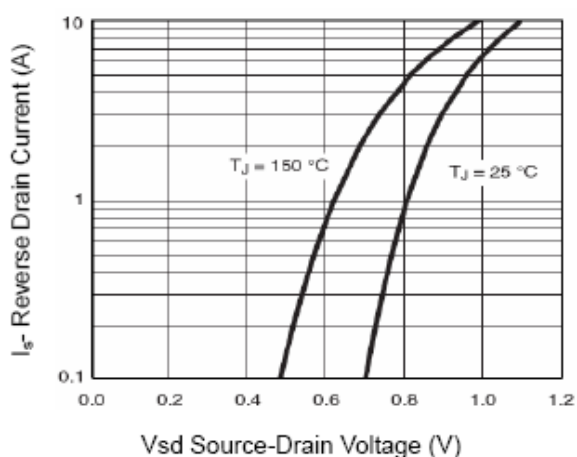


Figure 12 Source- Drain Diode Forward

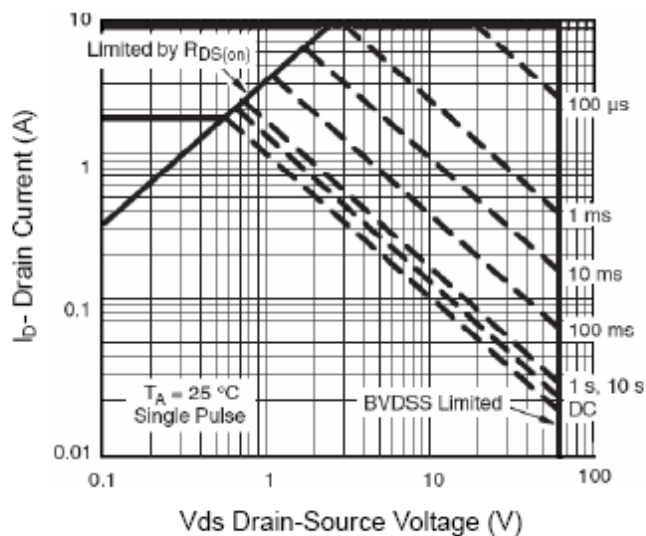


Figure 13 Safe Operation Area

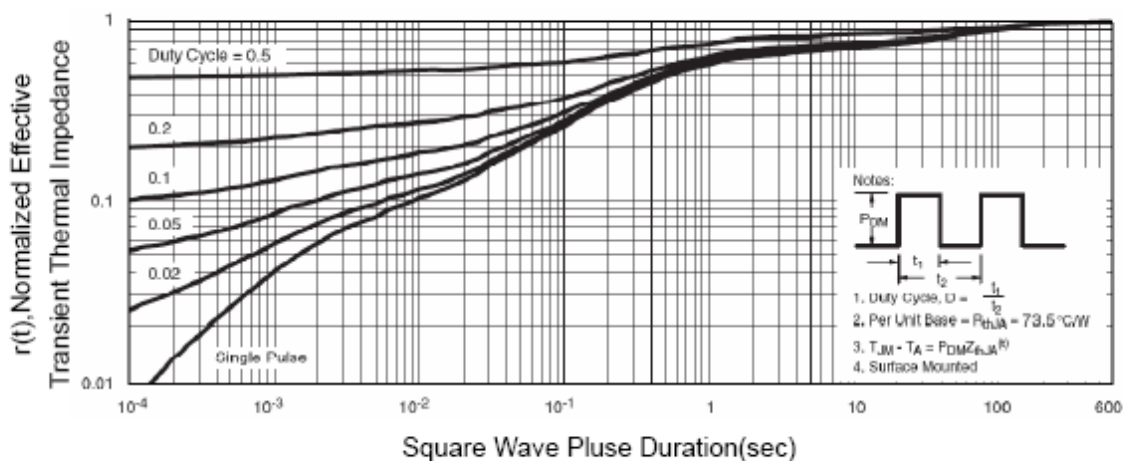


Figure 14 Normalized Maximum Transient Thermal Impedance

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