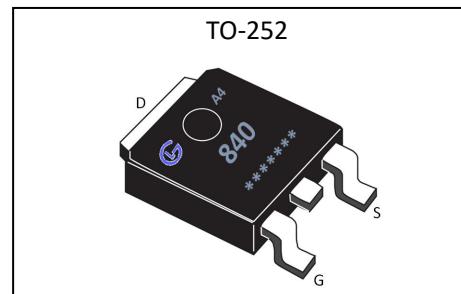


### General Description

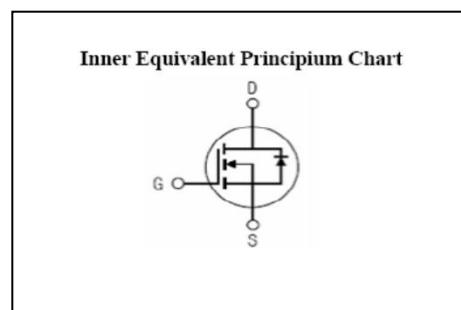
GL840A4 the silicon N-channel Enhanced VDMOSFETS, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-252, which accords with the RoHS standard.

$V_{DSS}$	500	V
$I_D$	8	A
$P_D$ ( $T_C=25^\circ\text{C}$ )	95	W
$R_{DS(\text{ON})}$	0.75	$\Omega$



### Features

- Fast Switching
- Low Gate Charge and  $R_{DSON}$
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test



### Applications

- Power switch circuit of adaptor and charger

### Absolute ( $T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	500	V
$I_D$	Continuous Drain Current	8	A
	Continuous Drain Current $T_c = 100^\circ\text{C}$	6.2	A
$I_{DM}^{a1}$	Pulsed Drain Current	32	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 30$	V
$E_{AS}^{a2}$	Single Pulse Avalanche Energy	750	mJ
$E_{AR}^{a1}$	Avalanche Energy ,Repetitive	70	mJ
$I_{AR}^{a1}$	Avalanche Current	3.5	A
$dv/dt^{a3}$	Peak Diode Recovery $dv/dt$	5.0	V/ns
$P_D$	Power Dissipation	95	W
	Derating Factor above $25^\circ\text{C}$	0.67	W/ $^\circ\text{C}$
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ\text{C}$
$T_L$	Maximum Temperature for Soldering	300	$^\circ\text{C}$

Caution :Stresses greater than those in the "Absolute Maximum Ratings" may cause permanent damage to the device



# GL840A4

GL Silicon N-Channel Power MOSFET

**Electrical Characteristics** ( $T_c = 25^\circ\text{C}$  unless otherwise specified)

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$V_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	500	--	--	V
$\Delta BV_{DSS}/\Delta T_J$	Bvdss Temperature Coefficient	$I_D=250\mu\text{A}$ , Reference $25^\circ\text{C}$	--	0.67	--	$\text{V}/^\circ\text{C}$
$I_{DSS}$	Drain to Source Leakage Current	$V_{DS}=500\text{V}, V_{GS}=0\text{V}, T_a=25^\circ\text{C}$	--	--	1	$\mu\text{A}$
		$V_{DS}=480\text{V}, V_{GS}=0\text{V}, T_a=125^\circ\text{C}$	--	--	100	
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+30\text{V}$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-30\text{V}$	--	--	100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10\text{V}, I_D=4.0\text{A}$	--	--	0.75	$\Omega$
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0	3.0	4.0	V
Pulse width $t_p \leq 380\mu\text{s}, \delta \leq 2\%$						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$g_{fs}$	Forward Transconductance	$V_{DS}=15\text{V}, I_D=4\text{A}$	--	10	--	S
$C_{iss}$	Input Capacitance	$V_{GS}=0\text{V}, V_{DS} = 25\text{V}$	--	1150	--	$\text{pF}$
$C_{oss}$	Output Capacitance	$f=1.0\text{MHz}$	--	90	--	
$C_{rss}$	Reverse Transfer Capacitance		--	11	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D=8.0\text{A}, V_{DD}=325\text{V}$	--	11	--	ns
$t_r$	Rise Time		--	21	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	36	--	
$t_f$	Fall Time		--	20	--	
$Q_g$	Total Gate Charge	$I_D=8.0\text{A}, V_{DD}=325\text{V}$	--	25	--	nC
$Q_{gs}$	Gate to Source Charge		--	6	--	
$Q_{gd}$	Gate to Drain ( "Miller" )Charge		--	9	--	

**Source-Drain Diode Characteristics**

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$I_S$	Continuous Source Current (Body Diode)		--	--	8	A
$I_{SM}$	Maximum Pulsed Current (Body Diode)		--	--	32	A
$V_{SD}$	Diode Forward Voltage	$I_S=8.0A, V_{GS}=0V$	--	--	1.5	V
$t_{rr}$	Reverse Recovery Time	$I_S=8.0A, T_j=25^\circ C$	--	280	--	ns
$Q_{rr}$	Reverse Recovery Charge	$dI_F/dt=100A/\mu s, V_{GS}=0V$	--	1.2	--	$\mu C$
Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$						

**Thermal Characteristics**

Symbol	Parameter	Typ.	Units
$R_{\theta JC}$	Junction-to-Case	1.32	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient	62	$^\circ C/W$

<sup>a1</sup>: Repetitive rating; pulse width limited by maximum junction temperature

<sup>a2</sup>:  $L=10.0mH, I_D=12.0A$ , Start  $T_j=25^\circ C$

<sup>a3</sup>:  $I_{SD} = 8.0A, dI/dt \leq 100A/\mu s, V_{DD} \leq BV_{DS}$ , Start  $T_j=25^\circ C$

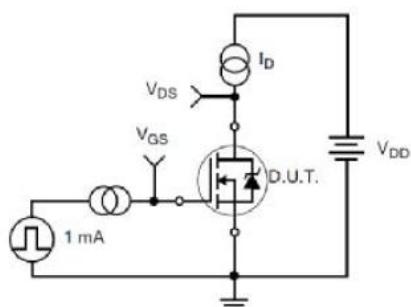
**Test Circuits**


Figure 17. Gate Charge Test Circuit

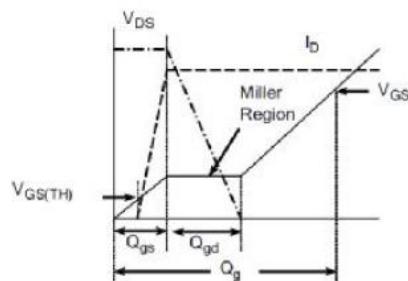


Figure 18. Gate Charge Waveform

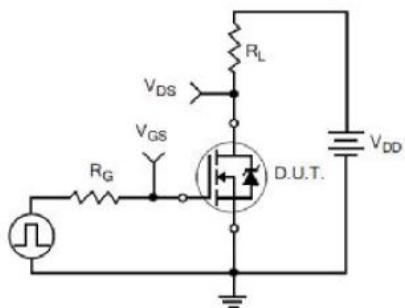


Figure 19. Resistive Switching Test Circuit

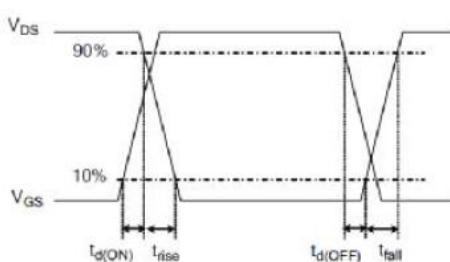


Figure 20. Resistive Switching Waveforms

### Characteristics Curves

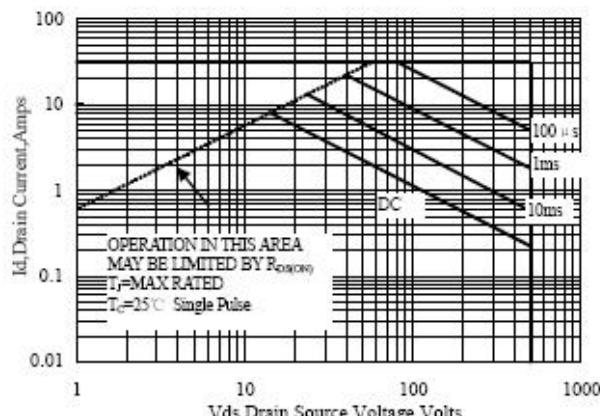


Figure 1 Maximum Forward Bias Safe Operating Area

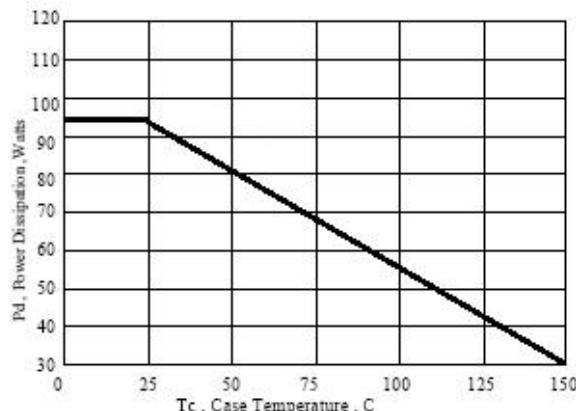


Figure 2 Maximum Power Dissipation vs Case Temperature

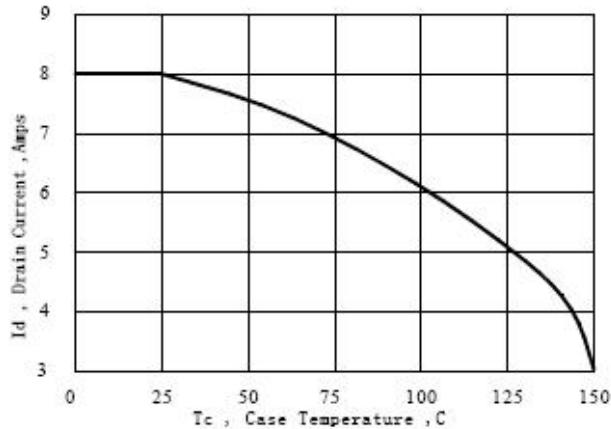


Figure 3 Maximum Continuous Drain Current vs Case Temperature

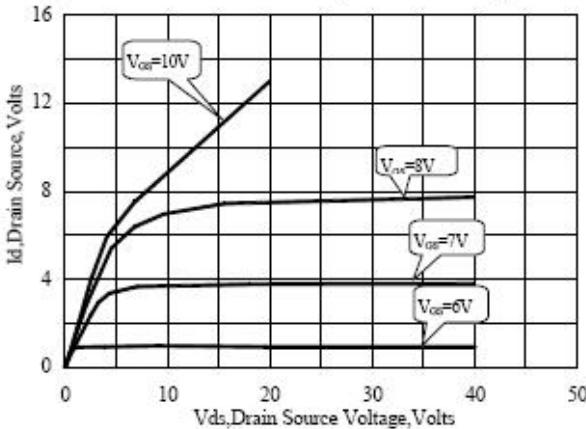


Figure 4 Typical Output Characteristics

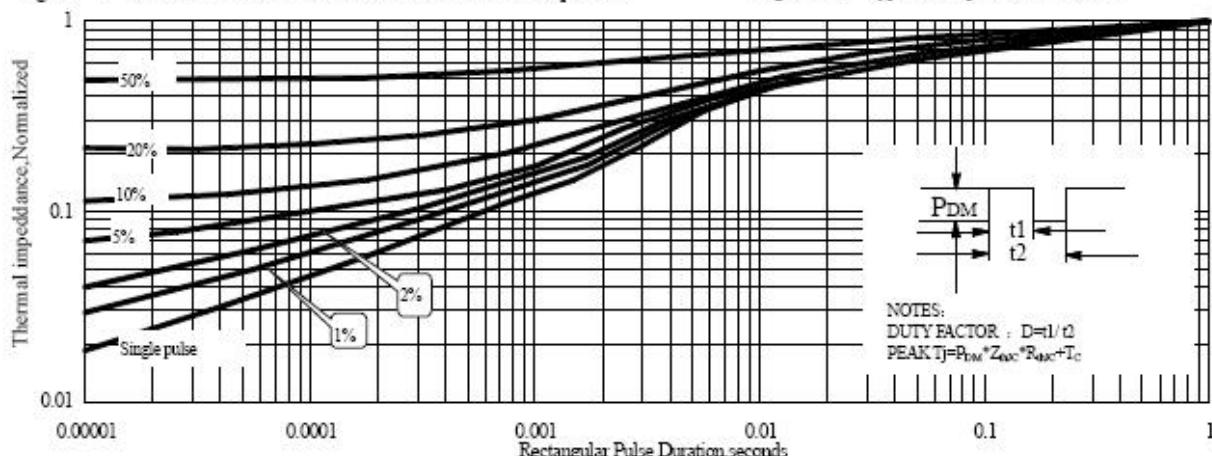


Figure 5 Maximum Effective Thermal Impedance, Junction to Case

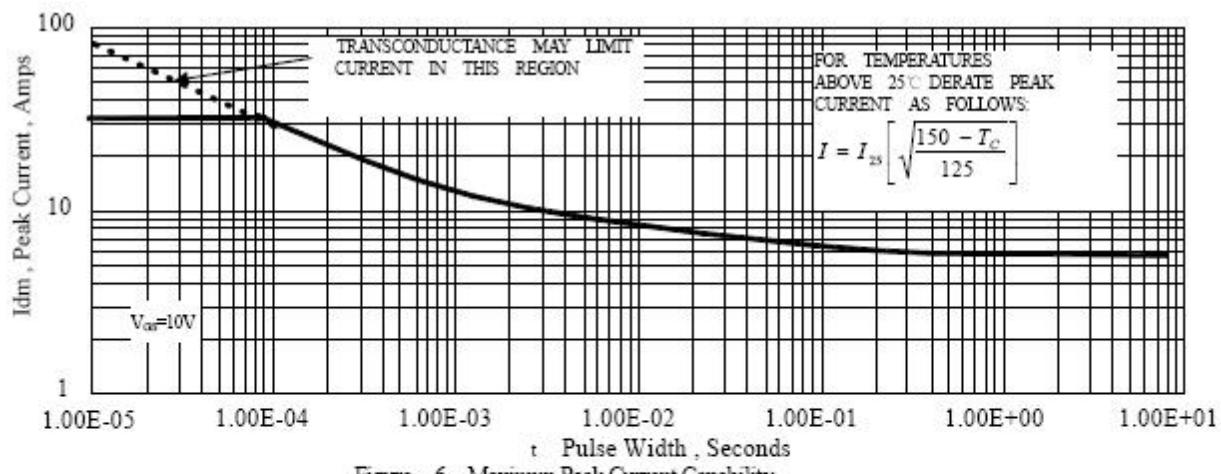


Figure 6 Maximum Peak Current Capability

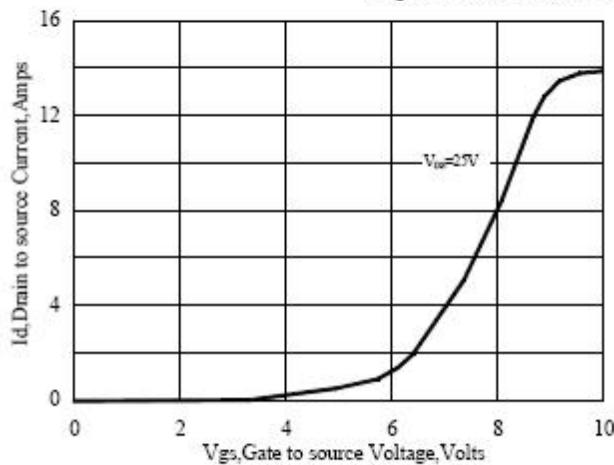


Figure 7 Typical Transfer Characteristics

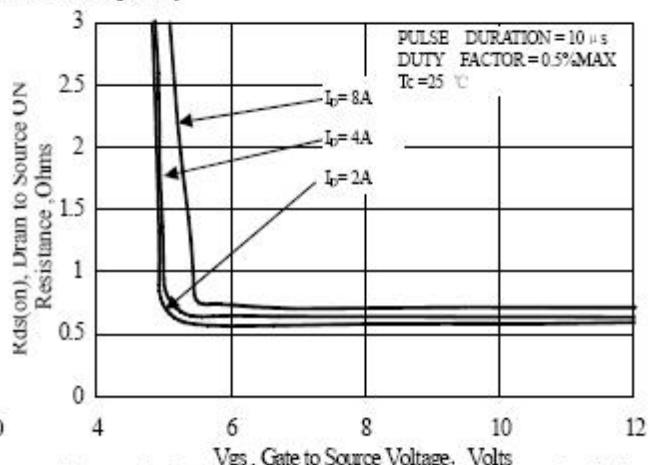


Figure 8 Typical Drain to Source ON Resistance vs Gate Voltage and Drain Current

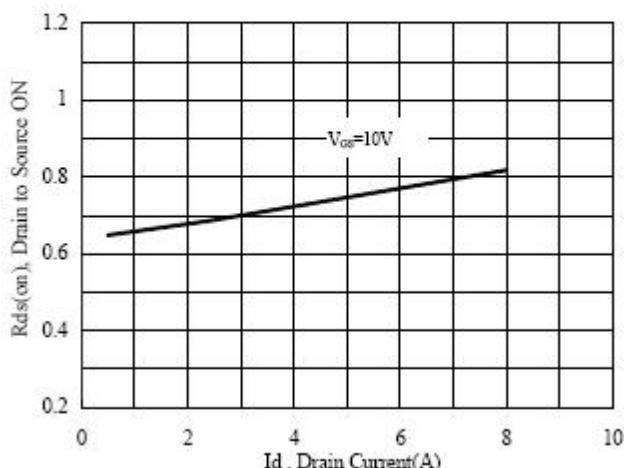


Figure 9 Typical Drain to Source ON Resistance vs Drain Current

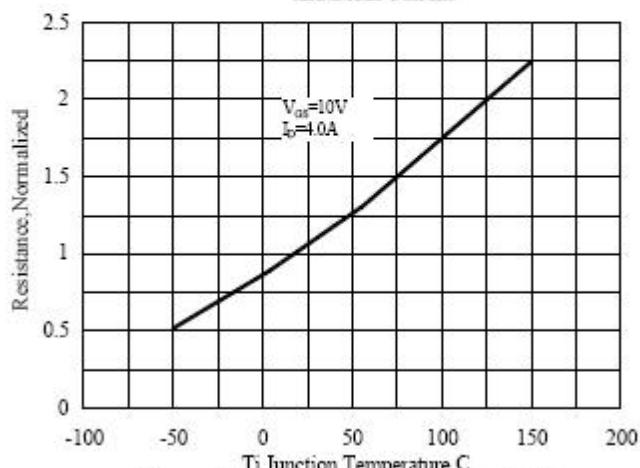


Figure 10 Typical Drian to Source on Resistance vs Junction Temperature

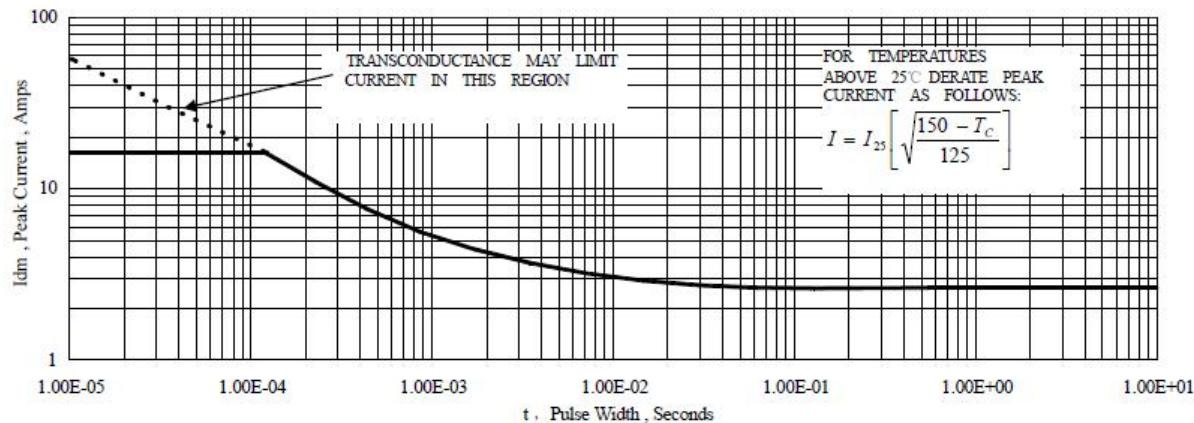


Figure 6 Maximum Peak Current Capability

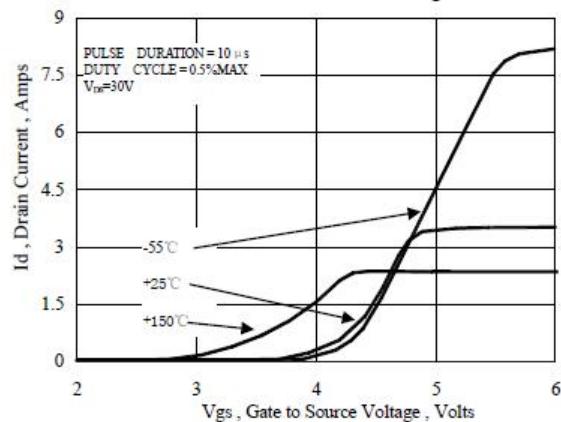


Figure 7 Typical Transfer Characteristics

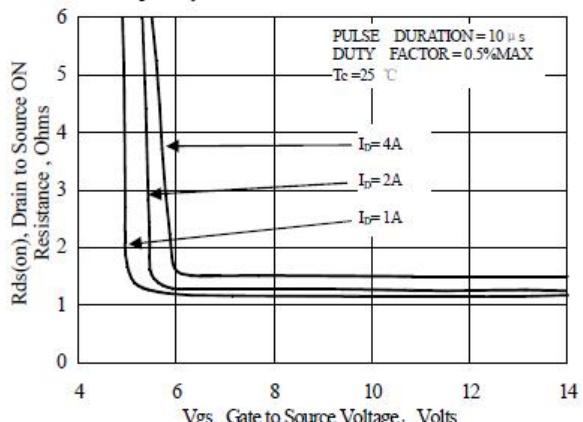


Figure 8 Typical Drain to Source ON Resistance vs Gate Voltage and Drain Current

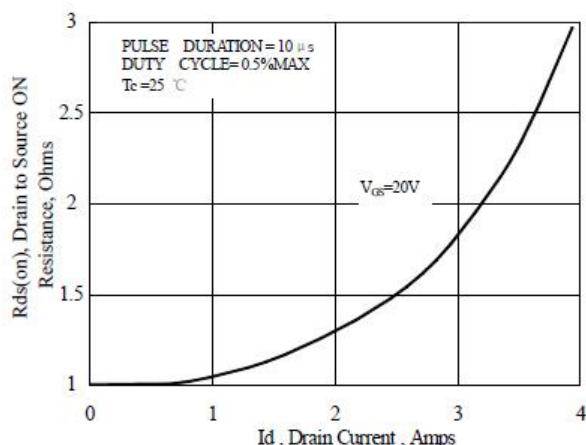


Figure 9 Typical Drain to Source ON Resistance vs Drain Current

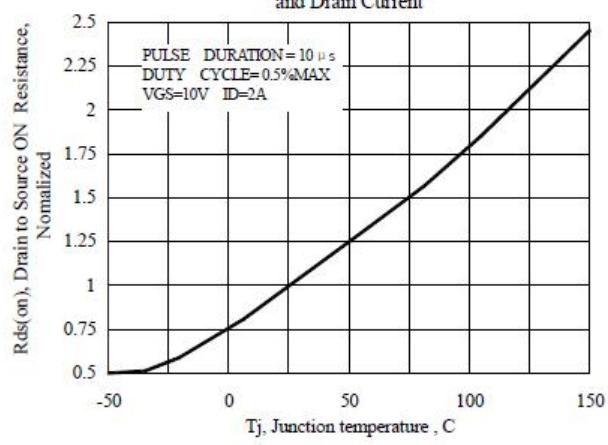


Figure 10 Typical Drian to Source on Resistance vs Junction Temperature