



GL10N70A0

GL Silicon N-Channel Power MOSFET

General Description:

GL10N70A0 the silicon N-channel Enhanced VDMOSFETS, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-263, which accords with the RoHS standard.

Features:

- Fast Switching
- Low Gate Charge and Rdson
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test

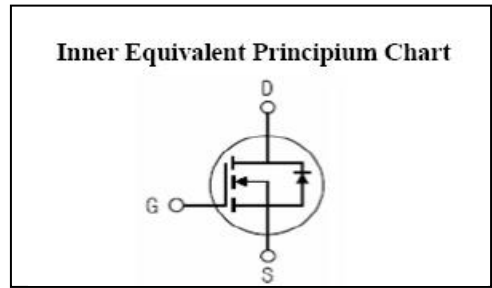
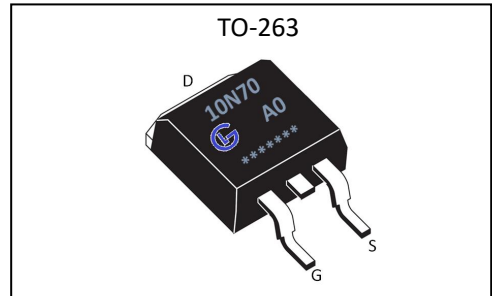
Applications:

Power switch circuit of adaptor and charger.

Absolute (Tc=25°C unless otherwise specified):

Symbol	Parameter	Rating	Units
V _{DSS}	Drain-to-Source Voltage	700	V
I _D	Continuous Drain Current	10	A
	Continuous Drain Current T _C = 100 °C	6.5	A
I _{DM} ^{a1}	Pulsed Drain Current	40	A
V _{GS}	Gate-to-Source Voltage	± 30	V
E _{AS} ^{a2}	Single Pulse Avalanche Energy	1000	mJ
E _{AR} ^{a1}	Avalanche Energy ,Repetitive	100	mJ
I _{AR} ^{a1}	Avalanche Current	4.5	A
dv/dt ^{a3}	Peak Diode Recovery dv/dt	5.0	V/ns
P _D	Power Dissipation	130	W
	Derating Factor above 25°C	1.04	W/°C
T _J , T _{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	°C
T _L	Maximum Temperature for Soldering	300	°C

V _{DSS}	700	V
I _D	10	A
P _D (T _C =25°C)	130	W
R _{DS(ON)} (TYP)	0.68	Ω





GL10N70A0

GL Silicon N-Channel Power MOSFET

Electrical Characteristics ($T_c=25^{\circ}\text{C}$ unless otherwise specified) :

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	700	--	--	V
$\Delta BV_{DSS}/\Delta T_J$	Bvdss Temperature Coefficient	$I_D=250\mu A$, Reference 25°C	--	0.74	--	V/ $^{\circ}\text{C}$
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=700V, V_{GS}=0V, T_a=25^{\circ}\text{C}$	--	--	1	μA
		$V_{DS}=560V, V_{GS}=0V, T_a=125^{\circ}\text{C}$	--	--	250	
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+20V$	--	--	10	μA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-20V$	--	--	-10	μA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10V, I_D=5A$	--	0.68	0.75	Ω
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	3.0	4.0	V
Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g_{fs}	Forward Transconductance	$V_{DS}=15V, I_D=5A$	--	11	--	S
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=25V$ $f=1.0\text{MHz}$	--	1900	--	pF
C_{oss}	Output Capacitance		--	170	--	
C_{rss}	Reverse Transfer Capacitance		--	14	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D=10A, V_{DD}=350V$ $V_{GS}=10V, R_G=4.7\Omega$	--	18	--	ns
t_r	Rise Time		--	32	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	52	--	
t_f	Fall Time		--	32	--	
Q_g	Total Gate Charge	$I_D=10A, V_{DD}=350V$ $V_{GS}=10V$	--	42	--	nC
Q_{gs}	Gate to Source Charge		--	9	--	
Q_{gd}	Gate to Drain ("Miller") Charge		--	16	--	

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)		--	--	10	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	40	A
V_{SD}	Diode Forward Voltage	$I_S = 10A, V_{GS} = 0V$	--	--	1.5	V
t_{rr}	Reverse Recovery Time	$I_S = 10A, T_J = 25^\circ C$	--	250	--	ns
Q_{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s, V_{GS} = 0V$	--	2.2	--	μC

Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$

Symbol	Parameter	Typ.	Units
$R_{\theta JC}$	Junction-to-Case	0.96	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient	62	$^\circ C/W$

^{a1}: Repetitive rating; pulse width limited by maximum junction temperature

^{a2}: $L = 10.0mH$, Start $T_J = 25^\circ C$

^{a3}: $I_{SD} = 10A, di/dt \leq 100A/\mu s, V_{DD} \leq BV_{DS}$, Start $T_J = 25^\circ C$

Test Circuit and Waveform

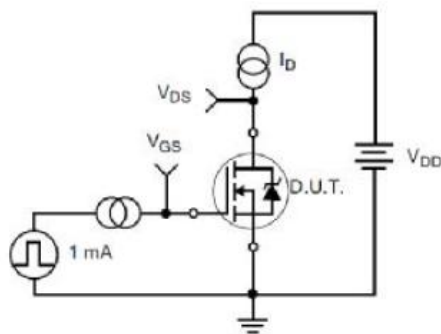


Figure 17. Gate Charge Test Circuit

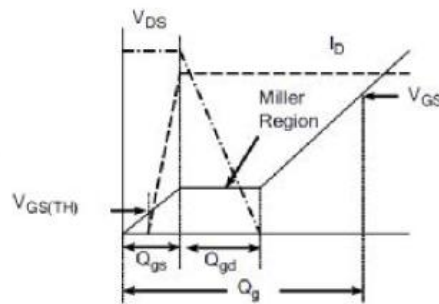


Figure 18. Gate Charge Waveform

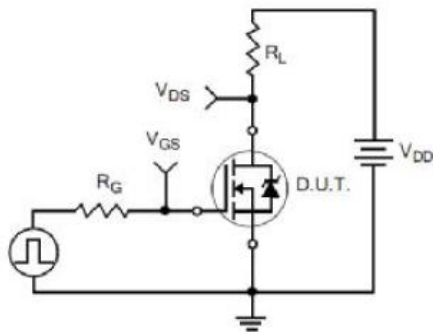


Figure 19. Resistive Switching Test Circuit

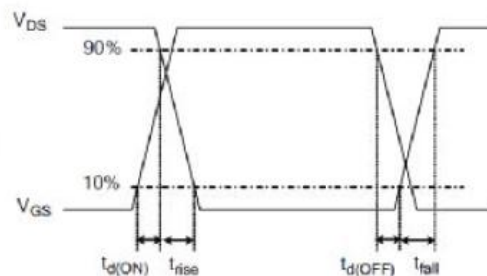


Figure 20. Resistive Switching Waveforms

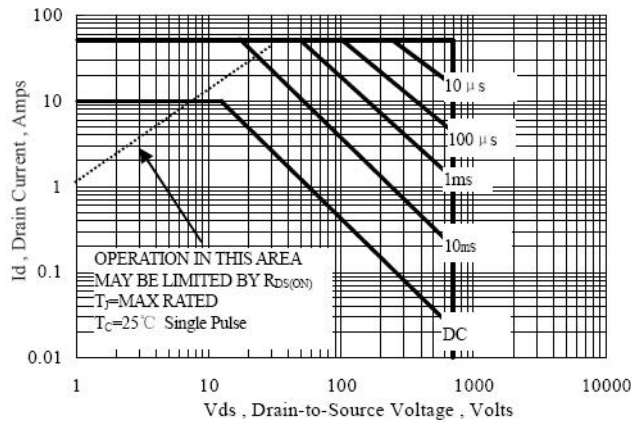


Figure 1 Maximum Forward Bias Safe Operating Area

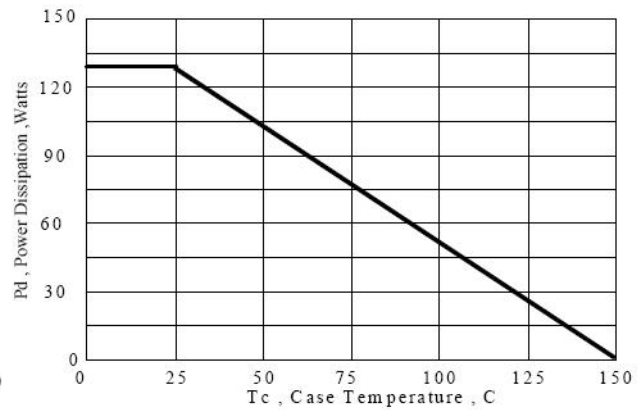


Figure 2 Maximum Power Dissipation vs Case Temperature

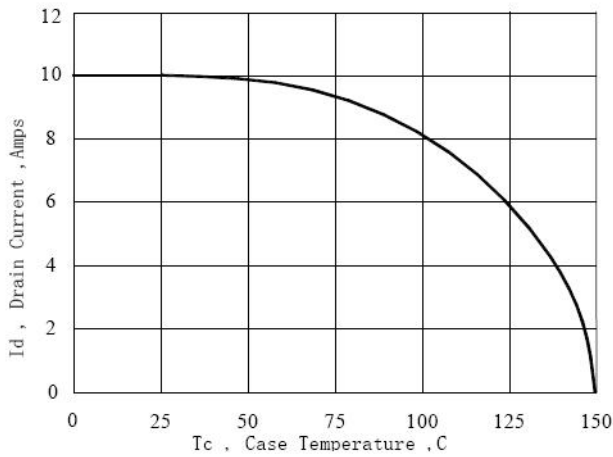


Figure 3 Maximum Continuous Drain Current vs Case Temperature

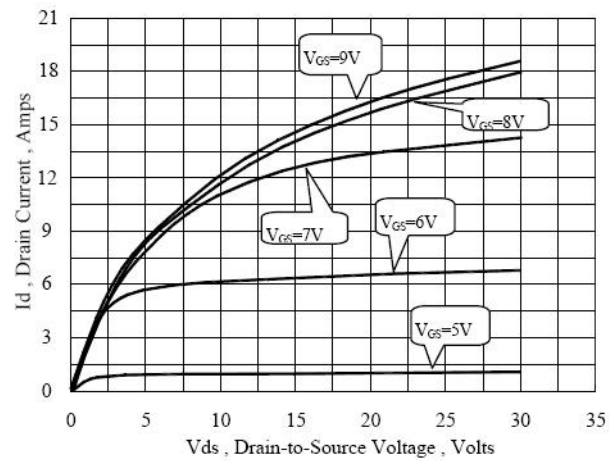


Figure 4 Typical Output Characteristics

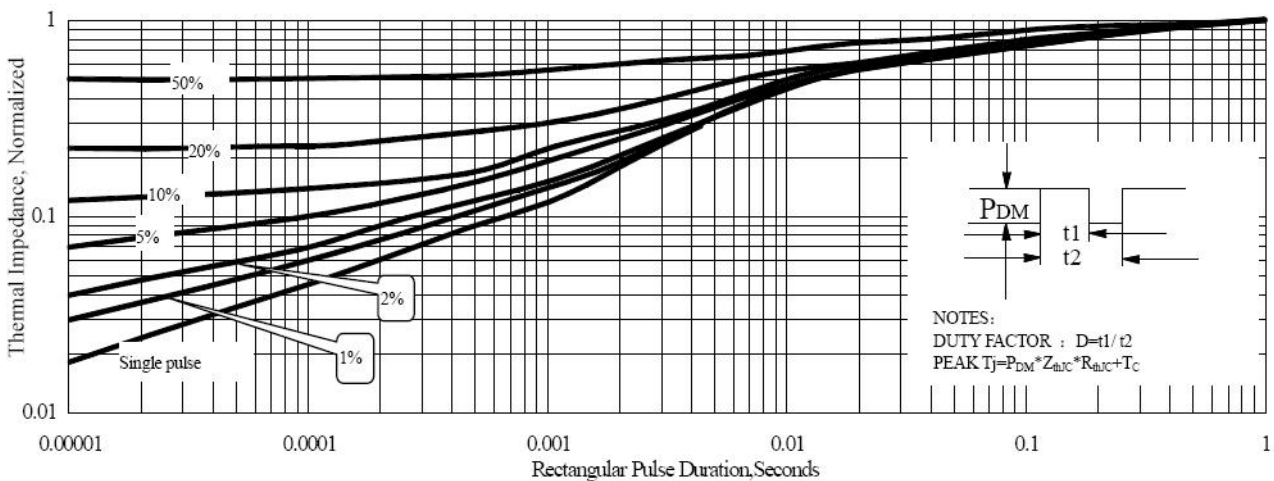
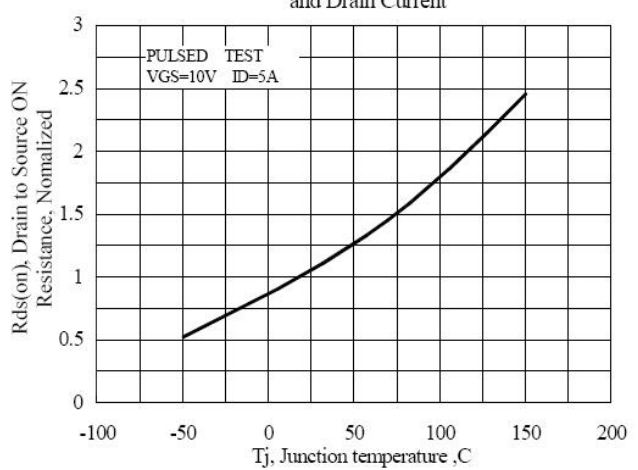
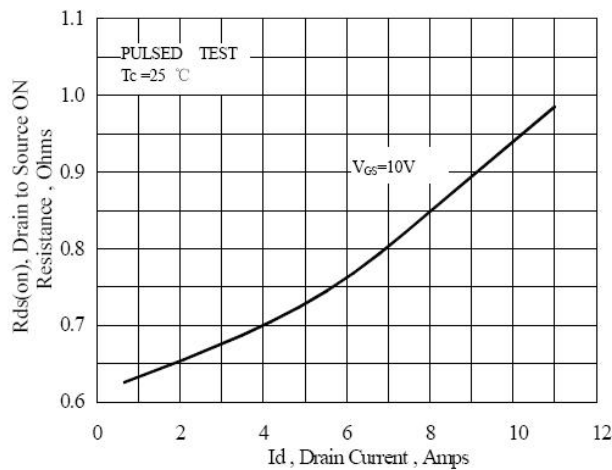
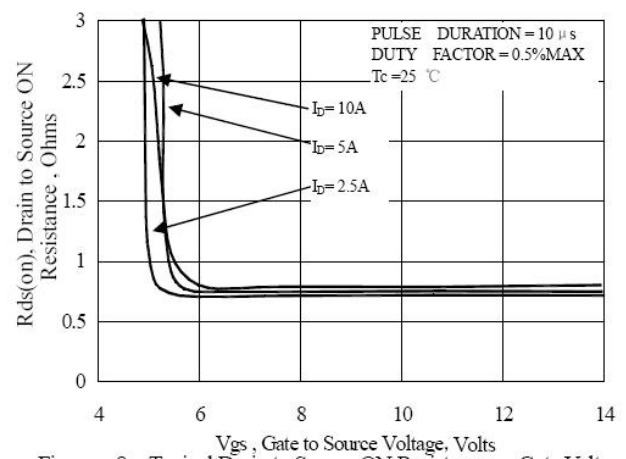
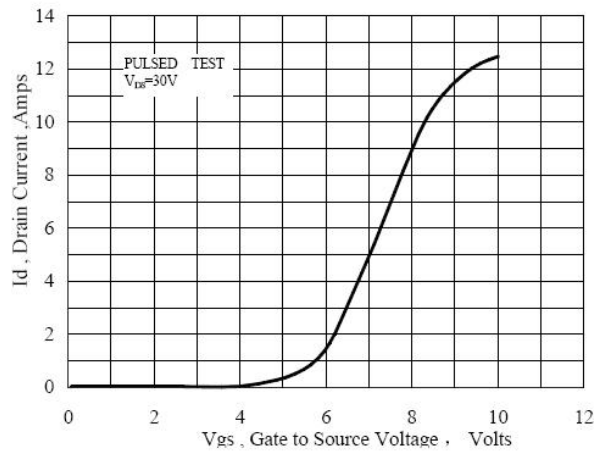
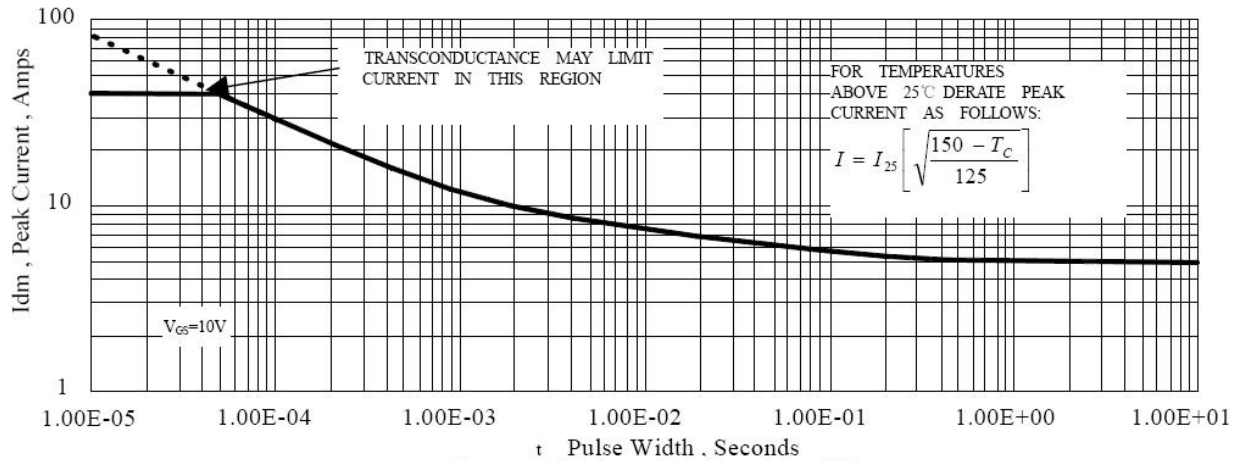


Figure 5 Maximum Effective Thermal Impedance, Junction to Case



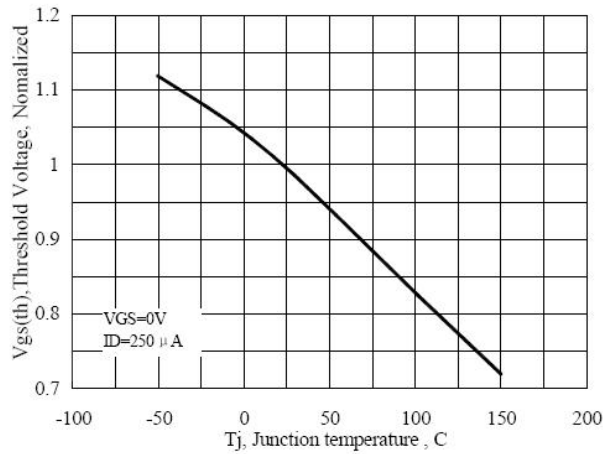


Figure 11 Typical Threshold Voltage vs Junction Temperature

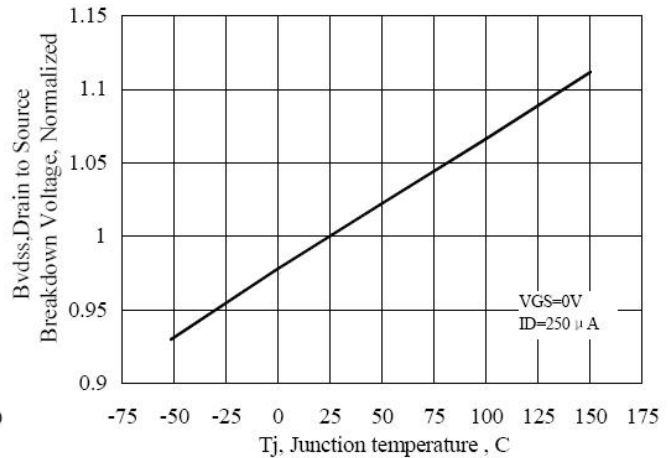


Figure 12 Typical Breakdown Voltage vs Junction Temperature

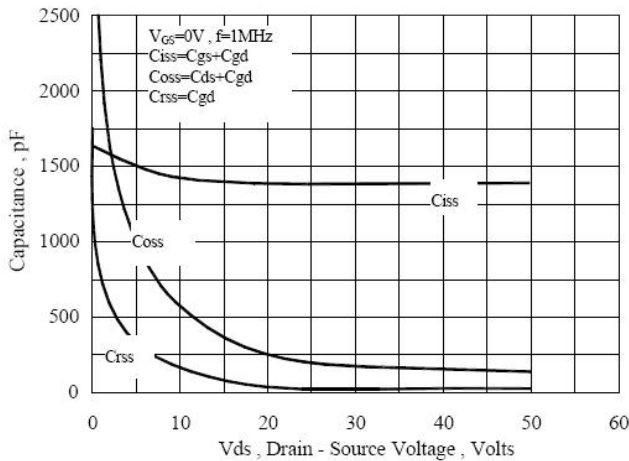


Figure 13 Typical Capacitance vs Drain to Source Voltage

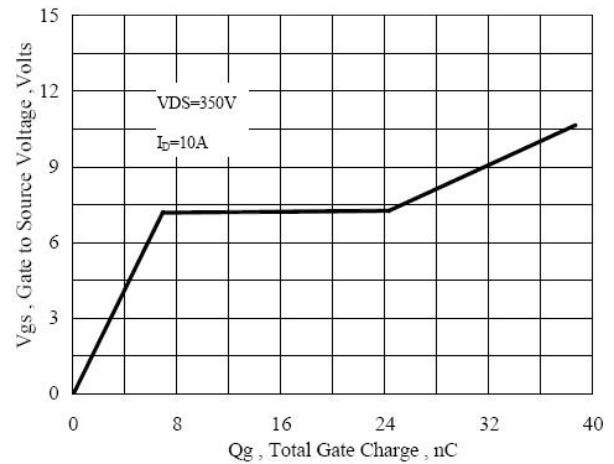


Figure 14 Typical Gate Charge vs Gate to Source Voltage

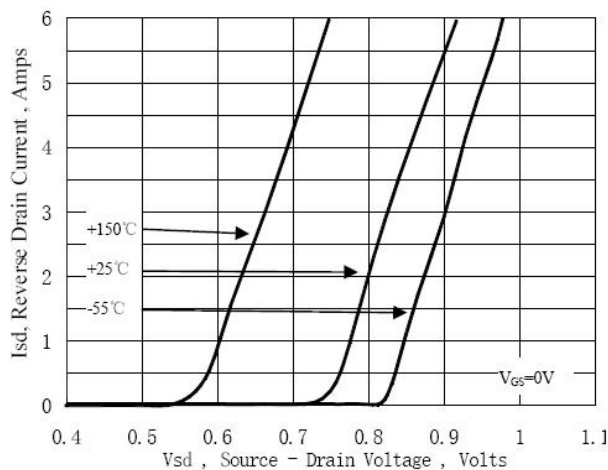


Figure 15 Typical Body Diode Transfer Characteristics

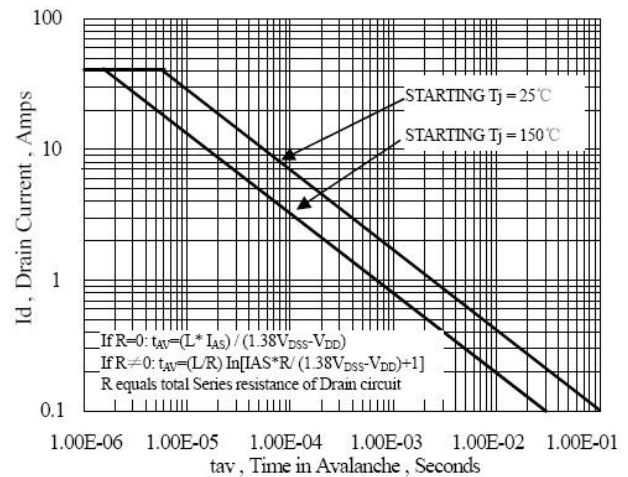


Figure 16 Unclamped Inductive Switching Capability