



# GL2300K

## GL Silicon N-Channel Power MOSFET

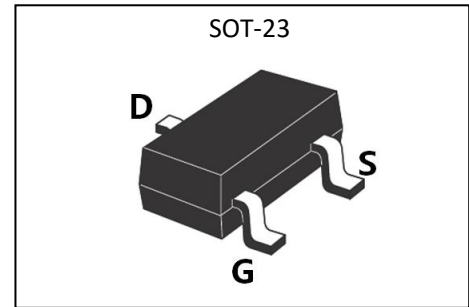
### General Description

The GL2300K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications. The package form is SOT-23, which accords with the RoHS standard.

$V_{DSS}$	20	V
$I_D$	6	A
$P_D$	0.4	W
$R_{DS(ON)type}$	32	m $\Omega$

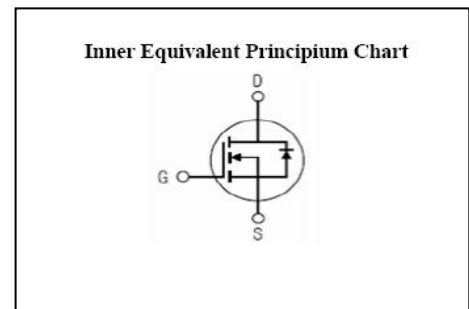
### Features

- $R_{DS(ON)} < 40m\Omega @ V_{GS}=4.5V$
- High density cell design for ultra low  $R_{dson}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation



### Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



### Absolute (Tc=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	20	V
$I_D$	Continuous Drain Current	6	A
$I_{DM}$	Pulsed Drain Current	18	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 8$	V
$P_D$	Power Dissipation	0.4	W
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	150, -55 to 150	$^{\circ}C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Units
$R_{\theta JA}$	Junction-to-Ambient <sup>a2</sup>	357	$^{\circ}C/W$



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### Electrical Characteristics (T<sub>c</sub>= 25°C unless otherwise specified)

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	20	--	--	V
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> =20V, V <sub>GS</sub> = 0V, T <sub>a</sub> =25°C	--	--	1.0	μA
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> = +8V	--	--	0.1	μA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> = -8V	--	--	-0.1	μA

ON Characteristics <sup>a3</sup>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3.0A	--	32	40	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =2.0A		40	55	mΩ
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =50μA	0.5	--	1.2	V
Pulse width tp ≤ 380μs, δ ≤ 2%						

Dynamic Characteristics <sup>a4</sup>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =3.0A	--	10	--	S
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =10V f=1.0MHz	--	500	--	pF
C <sub>oss</sub>	Output Capacitance		--	120	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	80	--	

Resistive Switching Characteristics <sup>a4</sup>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	V <sub>DD</sub> = -10V, R <sub>L</sub> =5.5Ω, I <sub>D</sub> =3A V <sub>GS</sub> =4.5V, R <sub>G</sub> =0.2Ω	--	8	--	ns
t <sub>r</sub>	Rise Time		--	30	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	22	--	
t <sub>f</sub>	Fall Time		--	10	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>SD</sub>	Diode Forward Voltage <sup>a3</sup>	I <sub>S</sub> =3A, V <sub>GS</sub> =0V	--	--	1.2	V

<sup>a1</sup>: Repetitive Rating: Pulse width limited by maximum junction temperature.

<sup>a2</sup>: Surface Mounted on FR4 Board, t<sub>s</sub> ≤ 10sec.

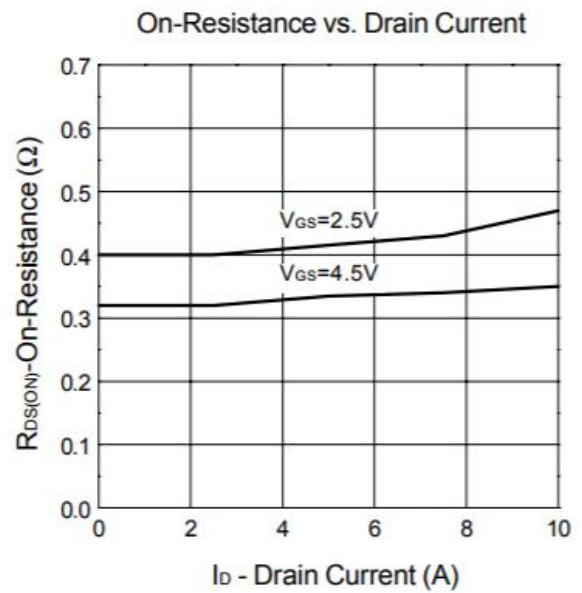
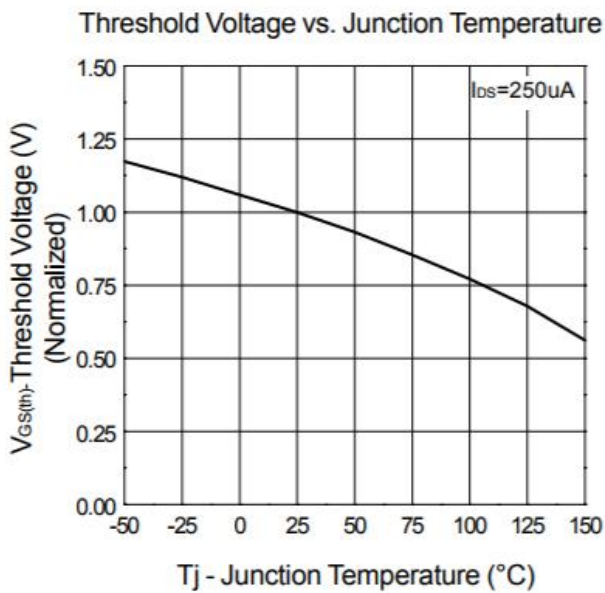
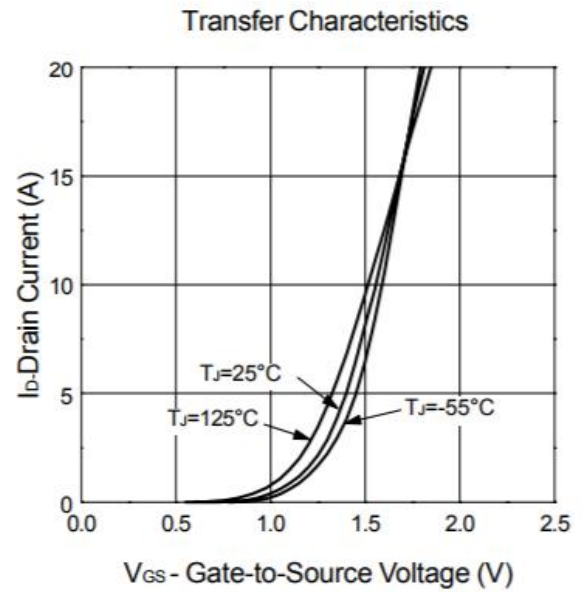
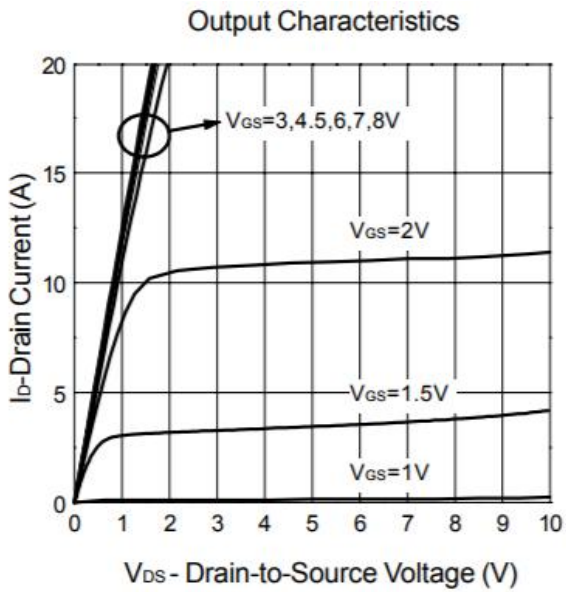
<sup>a3</sup>: Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.



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### Characteristics Curves

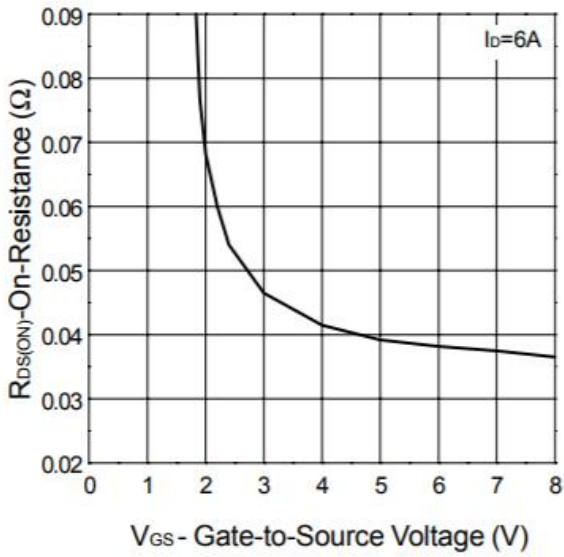




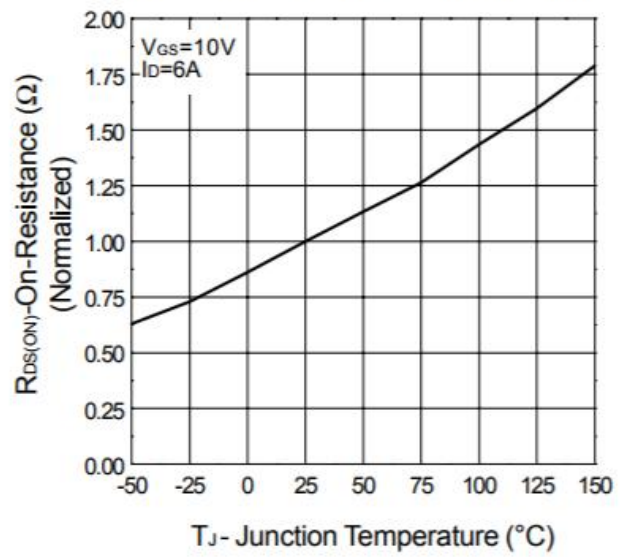
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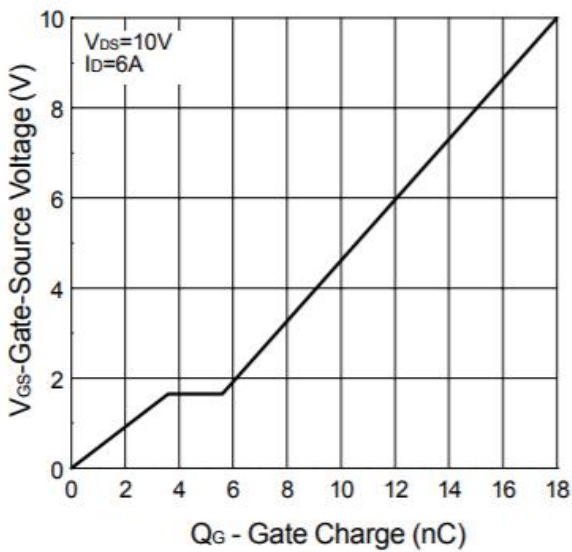
On-Resistance vs. Gate-to-Source Voltage



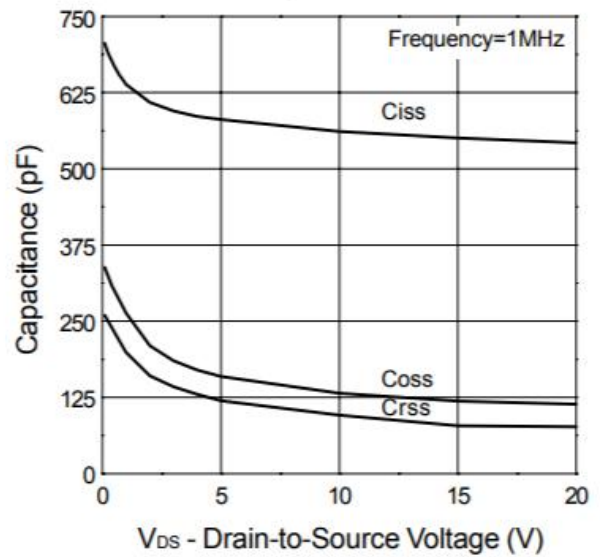
On-Resistance vs. Junction Temperature



Gate Charge



Capacitance





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